

Fig.1
(Prior Art)

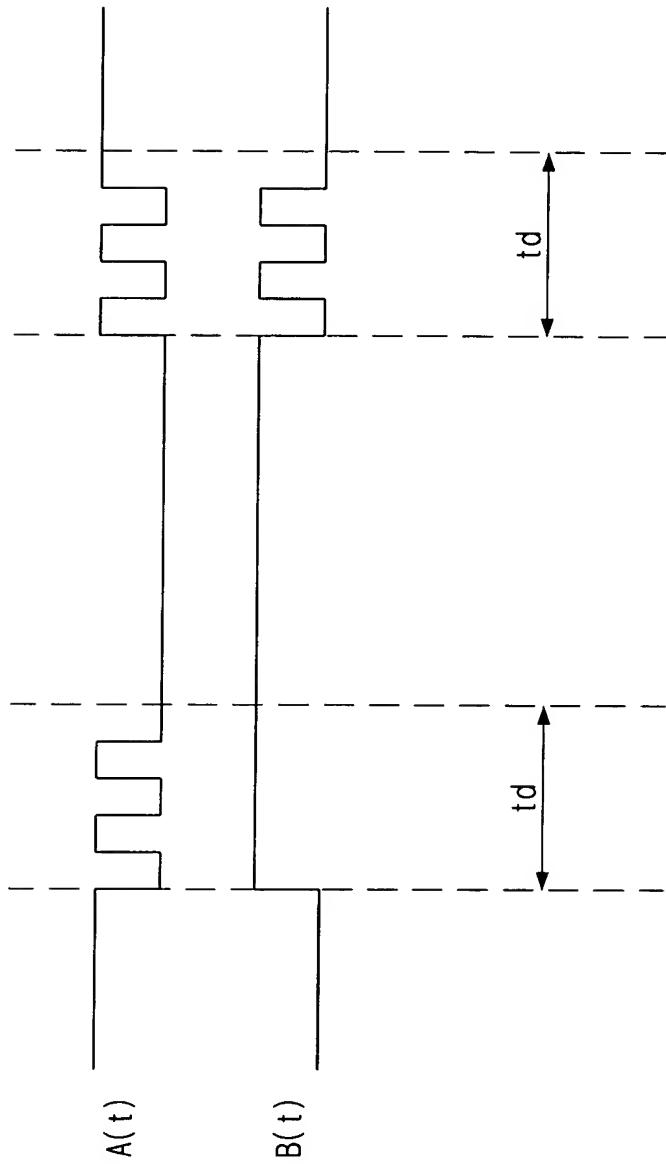


Fig.2
(Prior Art)

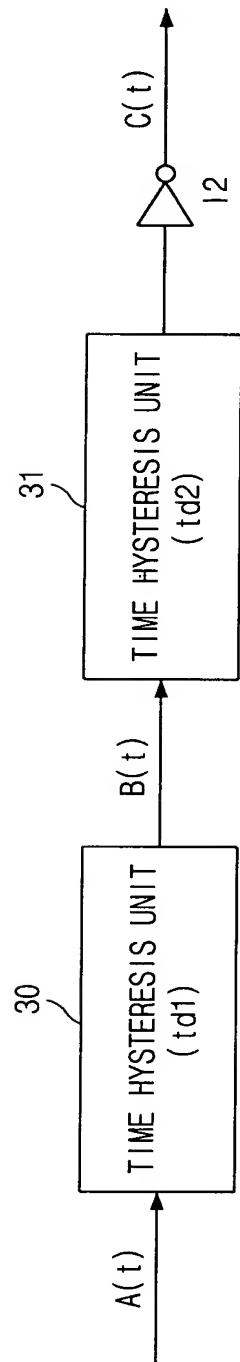


Fig. 3

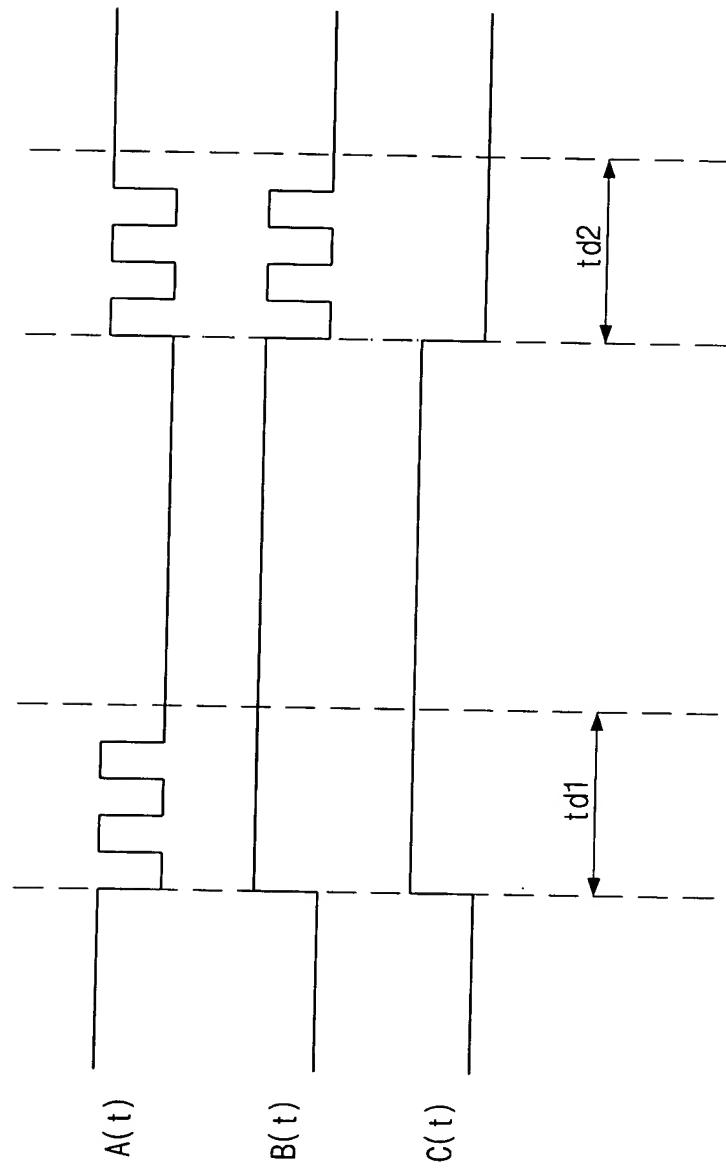


Fig. 4

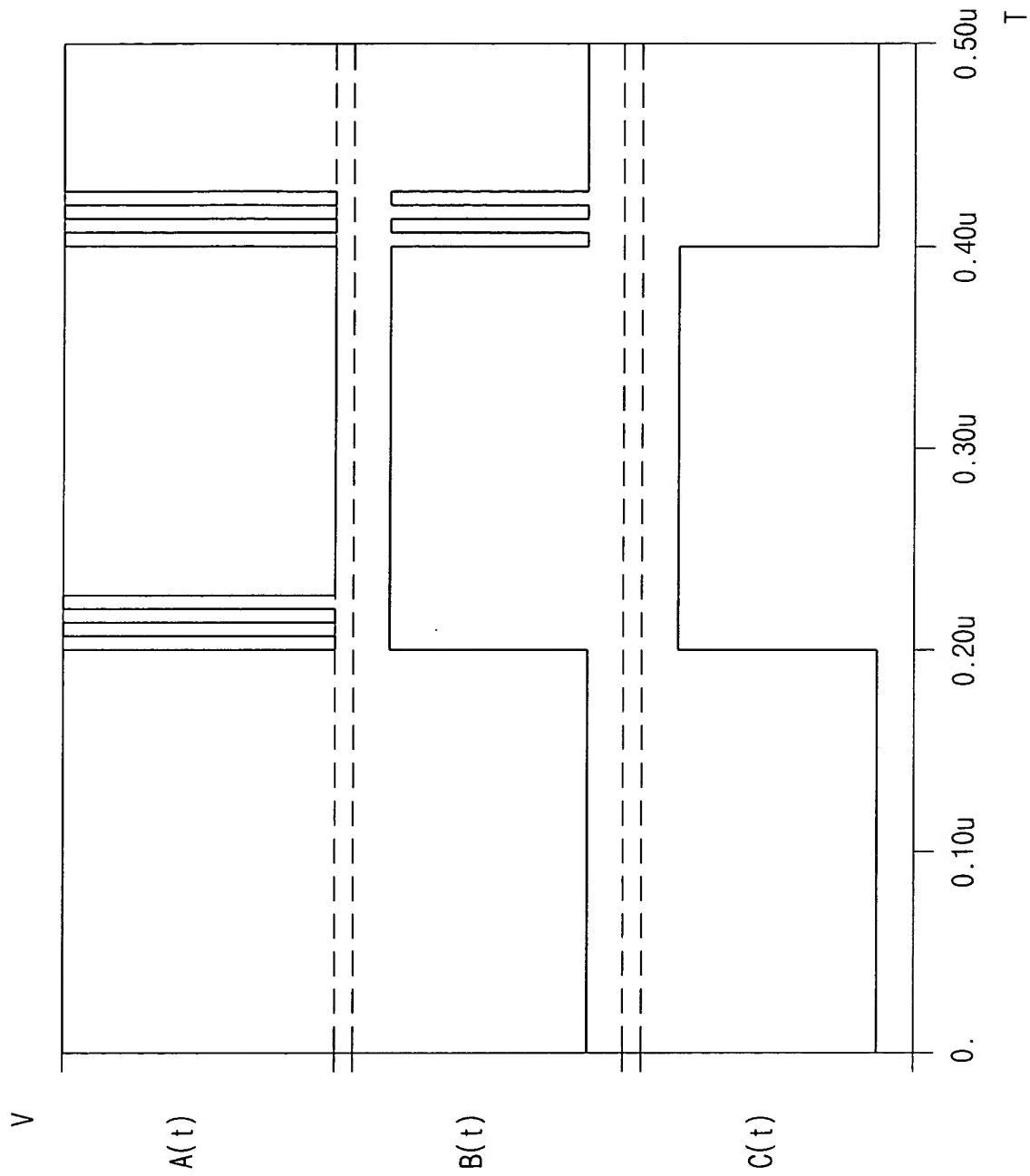


Fig. 5

Sang Sic YOON
"Digital Control Logic Circuit Having A Characteristic
of Time Hysteresis"
Attorney Docket No.: 40296-0013

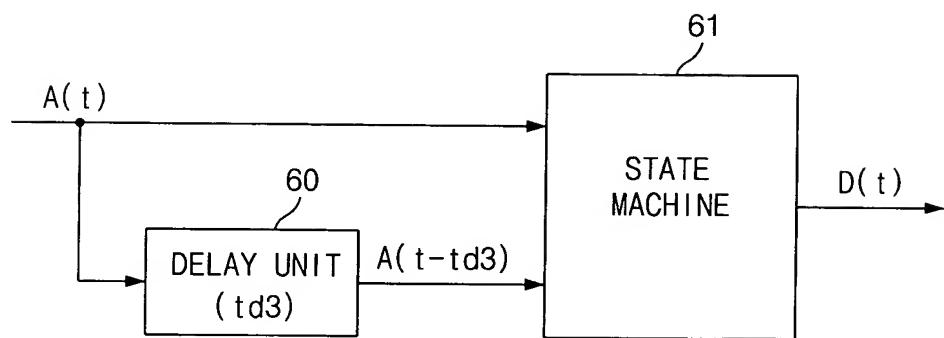


Fig.6

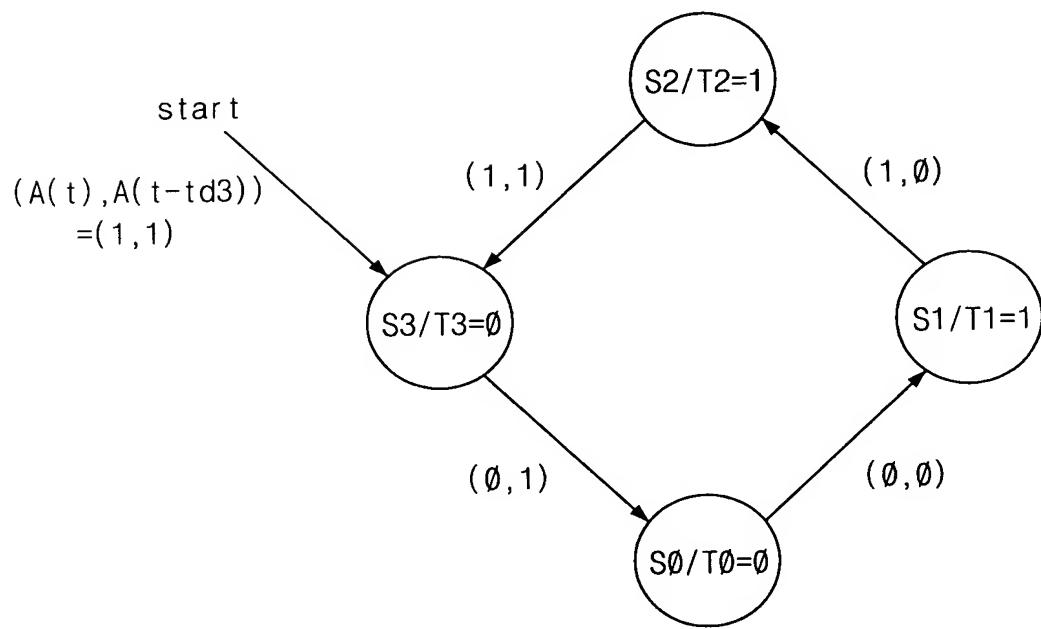


Fig.7

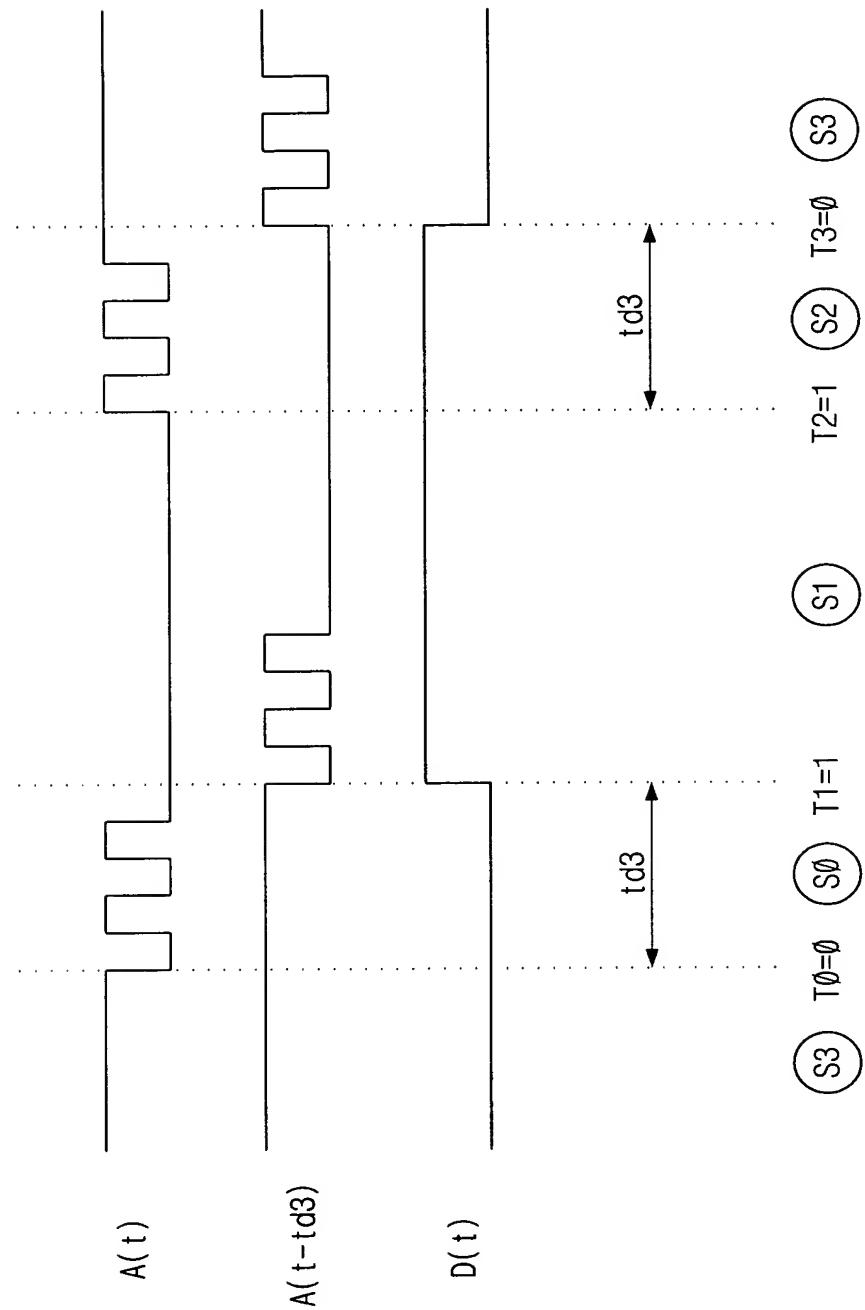


Fig. 8